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Blueprint Two

Working Session

26 February 2024 pre-read



Market event will include EDI, testing and cutover updates



Draft bespoke EDI mappings shared with relevant vendors from 16 February



Rigorous testing programme underway to deliver robust system when live



Velonetic will be ready to cutover to phase one digital services from 1 July



Market cutover will begin on 9 July to allow for quarter end activities

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EDI messages

All standard & bespoke EDIs will be published by 6 March

	EDI committed	Specs ready/ in review with ASG	Specs pending	Published	Target publication date	Additional updates
Standard	31	0	1	30	1 March ¹	<ul style="list-style-type: none"> Pending USM_V9 following vendor feedback
Bespoke	22	21	1	0	6 March	<ul style="list-style-type: none"> 21 of bespoke EDIs mapped and in review with ASG Drafts will be released to vendors by 16 February Targeting 6 March for final publication (may come forward and if so, may be released in increments)
ACORD4ALL	18	0	18	0	-	<ul style="list-style-type: none"> Update on publication will be shared prior to 26th February

1. Subject to vendor agreement

We've carefully reviewed additional EDI requests



Accepted requests

EDI - LORS XML is supported and now in scope



In review

Request for dispatcher processes - C_Expand is still under review and decision will be confirmed on 19th February



Declined

Request for LORS ETS has been declined and will not be added to the phase one bespoke EDIs

We cannot guarantee that any further requests will be considered

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End-to-end testing

On track to deliver a robust system in time for cutover



A rigorous, layered testing approach is being deployed, with multiple test phases

Well-defined testing framework in place for DXC, Velonetic, Lloyd's and market participants



We're adhering to a well-defined defect management procedure

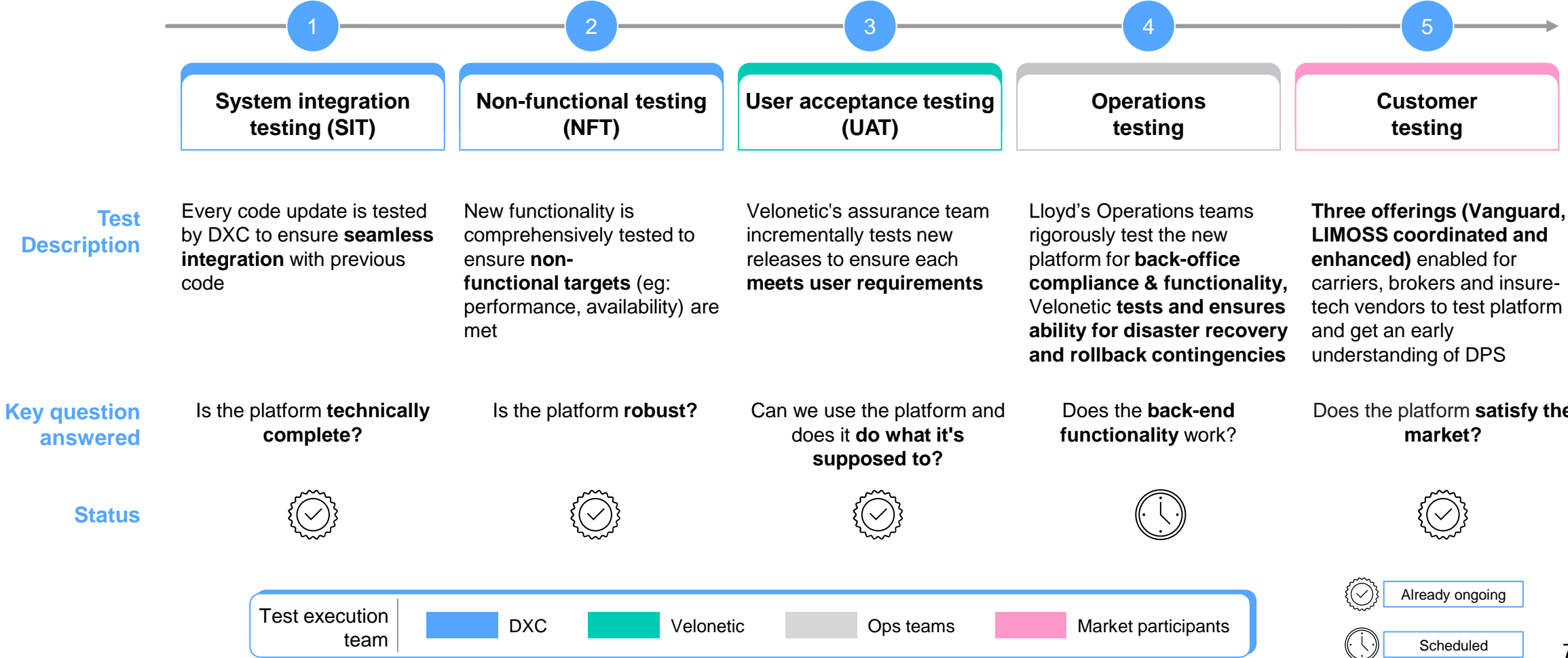


System integration, user acceptance, non-functional and Vanguard testing already ongoing

Testing progressing well, with Vanguard participants and environment finalised and execution kicked off further testing

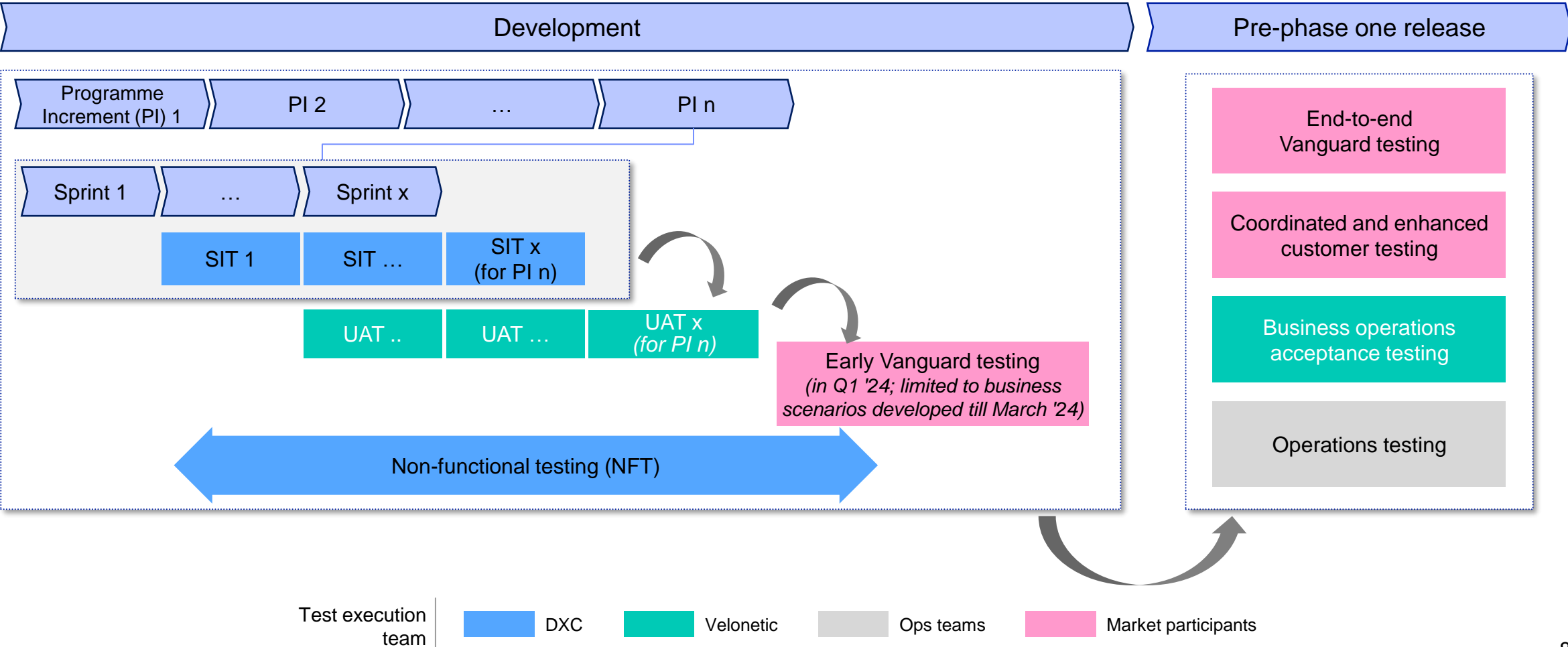
Five layers of testing to ensure a stable, resilient and fit-for-purpose solution

Note: Sequencing of test types illustrated on next page



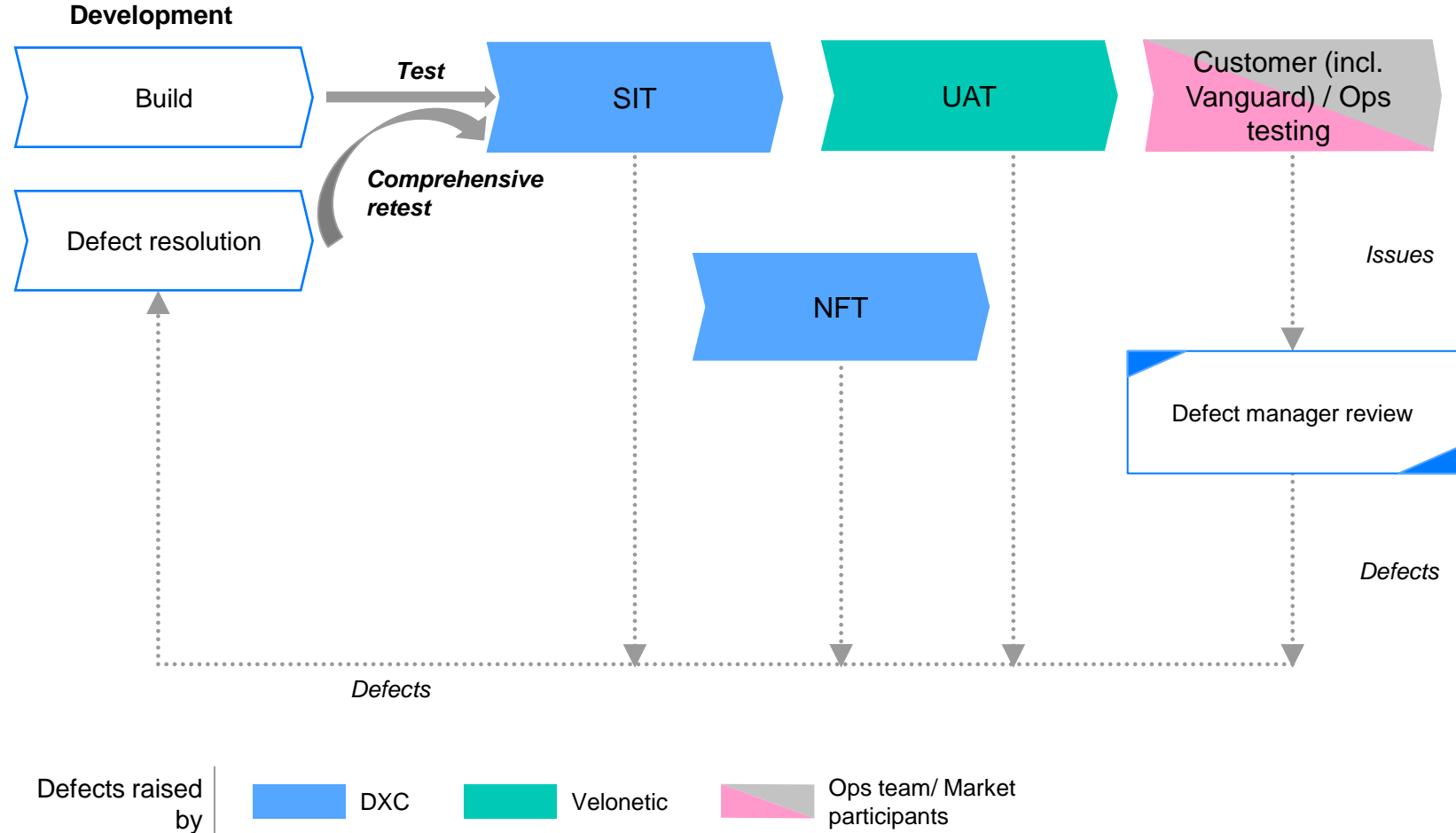
Rigorous end-to-end testing framework in place

End-to-end testing framework



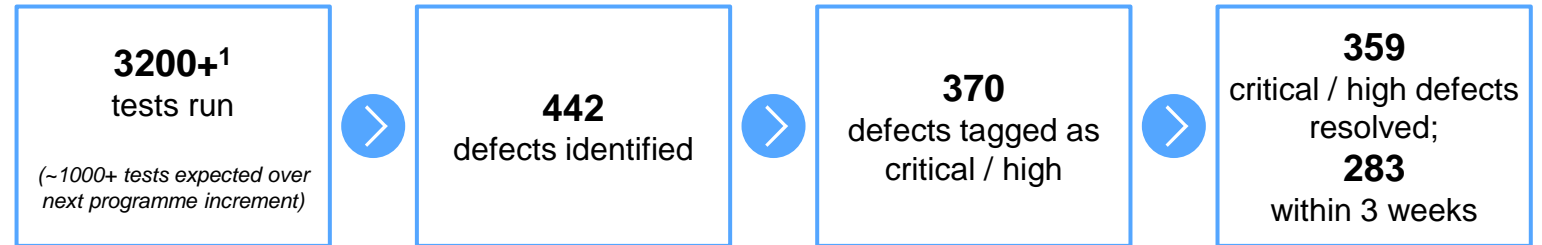
If defects are found, we have a well defined resolution process in place

Defect management process up to cutover



- Defects identified in SIT, UAT, and NFT enter the build backlog for fixing, post severity / priority assessment
- Issues in other tests are triaged first by a defect manager before being added to the backlog
- Resolved defects undergo retesting through the test phases, as part of the route to live

Key metrics from tests run as of 9 February 2024



Analysis of SIT defects by criticality and aging

	Critical / high priority defects		Medium / low priority defects		Total
	Resolved	Unresolved	Resolved	Unresolved	
<3 weeks	283	5	34	1	323
3-6 weeks	17		11	2	30
6-12 weeks	4	2	6	1	13
>12 weeks	55	4	14	3	76
Total	359	11	65	7	442

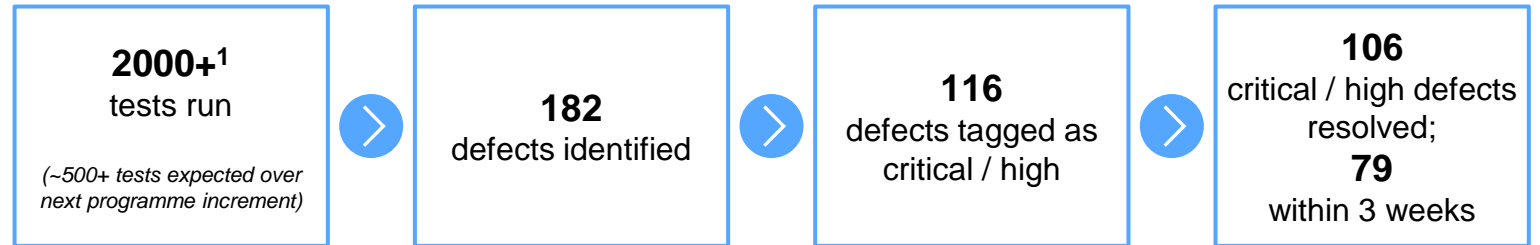
1. Tests in the final cycle (including regression tests) for the PI considered

Note: Defects as of 9 Feb 2024 analysed; Defects with status "Done", "Resolved" considered "Resolved"; Critical and high priority defects also include defects tagged as blocker, major; Medium and low priority defects also include defects tagged as minor

Source: SIT test completion reports for PI 7/8, Global PI 8 Fast follow, PI 9, PI 10, PI 11

Status update | SIT summary

Key metrics from tests run as of 8 February 2024



Analysis of UAT defects by criticality and aging

	Critical / high priority defects		Medium / low priority defects		Total
	Resolved	Unresolved	Resolved	Unresolved	
<3 weeks	79	8	17	4	108
3-6 weeks	9		10		19
6-12 weeks	7		3	1	11
>12 weeks	11	2	20	11	44
Total	106	10	50	16	182

1. As of 8th February

Note: Defects as of 8 February analysed; Defects with status "Done", "Resolved" considered "Resolved"; Critical and high priority defects also include defects tagged as blocker, major; Medium and low priority defects also include defects tagged as minor

Source : UAT test completion reports for Global, Fast-follow, PI 9 and PI 10, Defect logs on JIRA

Status update | UAT Summary

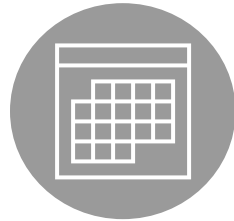
*NFT and Vanguard testing metrics to be consolidated,
post completion of their respective testing runs*

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Cutover plan

Velonetic will be ready on 1 July; market cutover begins on 9 July to enable quarter end batch processing



1 July

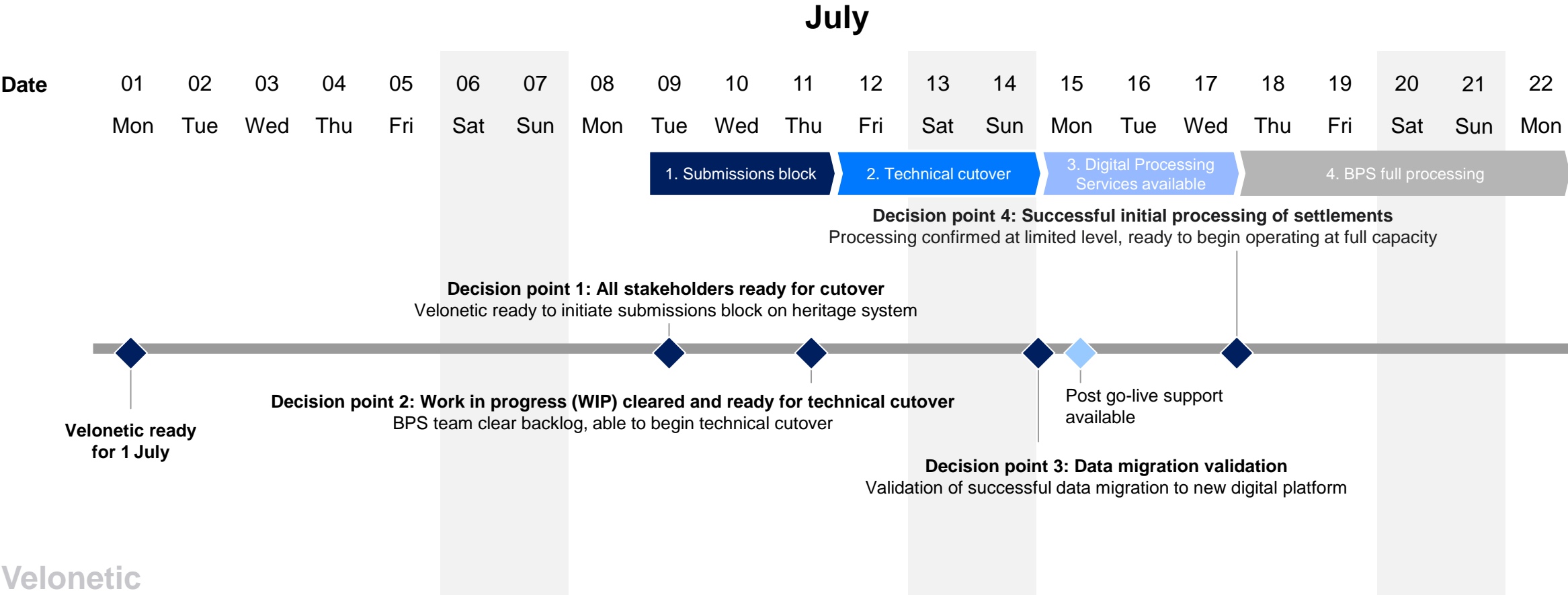
- **Velonetic will be ready for 1 July as planned**
- End of Q2 processing must take place at the beginning of July
- It is preferable for cutover to begin as soon as quarter end processing and reporting is complete



9 July

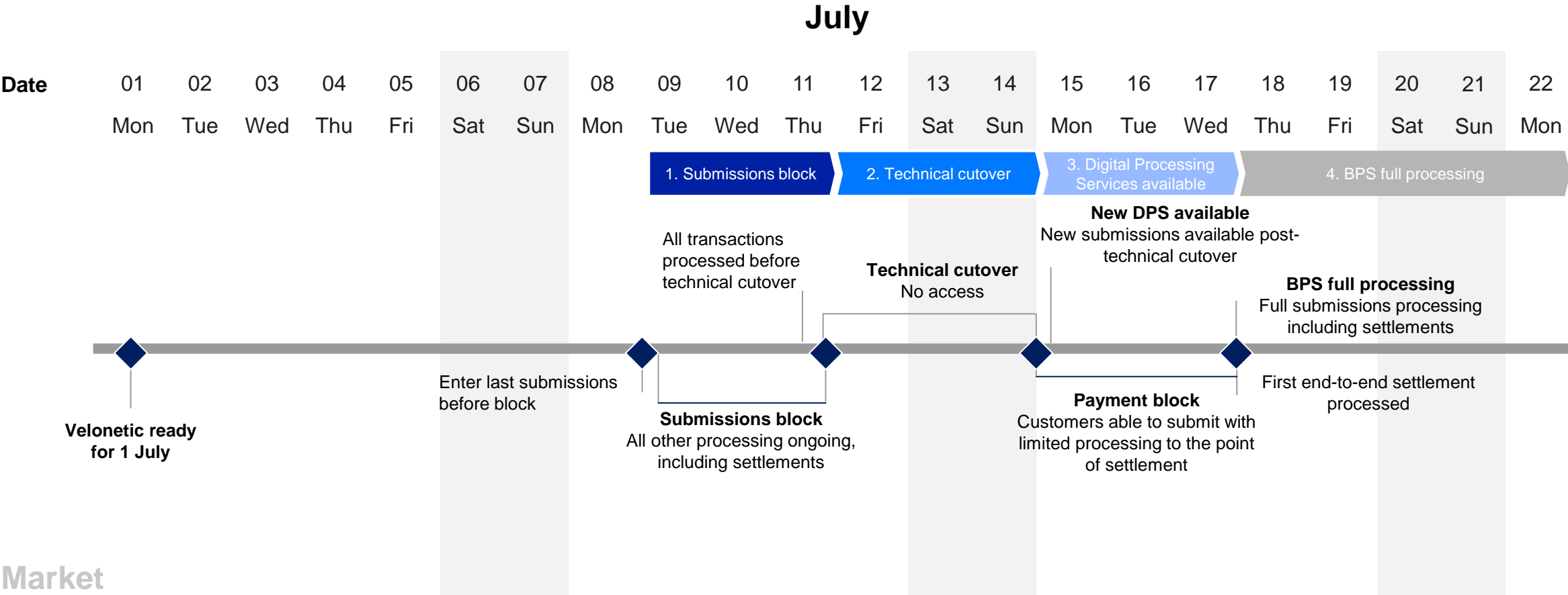
- **Market cutover will begin on 9 July**
- This enables quarter end batch run to go ahead as planned on 5/6 July and proceed without incident
- No distraction from the preparation and execution of an earlier quarter end

Velonetic will pass four decision points in cutover window



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Key cutover milestones for market participants





Appendix

Glossary of key terms

Term	Acronym	Description
Cutover		The transition process from heritage to DPS systems, starting from submissions block and culminating in return to full processing on DPS
Programme increment	PI	A 12-week timebox for build delivery
In-sprint testing	-	Component and system testing, executed by DXC
System integration testing	SIT	Tests if the new functionality integrates correctly, and the end-to-end business process flow works as expected
User acceptance testing	UAT	Tests the features of the platform using generic data (assessing the master business scenarios)
Non-functional testing	NFT	Tests if code meets availability, performance, security, usability, accessibility, recoverability, supportability, data and maintenance targets
Vanguard	-	Small group of market participants testing specific scenarios to ensure the new platform is fit-for-purpose and meets user requirements
Coordinated and enhanced customer testing	-	Tests if the new platform can connect to and run business processes correctly with end-user systems (more about giving confidence & early visibility to the platform)
Business operations acceptance testing	-	Tests if Velonetic operations are prepared for platform change
Lloyd's operations readiness testing	-	Tests if Lloyd's systems can integrate with the new platform, and operations are prepared
Vendor testing	-	Tests that vendors are still able to connect & provide functional services post-cutover

Full list of 31 standard EDIs committed

Message	Direction	Type
WSETT	Outgoing	Settlement
DSIGN	Outgoing	Signing
MCM	Outgoing	Controlled
USM	Outgoing	Signing
ILUCSB	Outgoing	Settlement
ILUCSM	Outgoing	Signing
CSMCST	Outgoing	Signing
IPCCSM	Outgoing	Delinked Signing
SCM	Outgoing	Claims
BSM	Outgoing	Signing
IPCBSM	Outgoing	Delinked Signing
IPCDSM	Outgoing	Delinked Signing
LIMCLM (CLMLRC)	Outgoing	Claims
LIMCLM (CLMILC)	Outgoing	Claims
LIMRIS (Carrier)	Outgoing	LORS
LIMRIS (Broker)	Outgoing	LORS

Message	Direction	Type
REDIAL	Outgoing	Delinked Settlement
LIMRES (LIMCLM)	Outgoing	Error/Confirmation
LIMCLM(CLMLRB)	Outgoing	Claims
LIMCLM(CLMLLB)	Outgoing	Claims
LIMCLM(CLMLLB)	Outgoing	Claims
LIMCLM(CLMLLB)	Outgoing	Claims
LIMRIA	Outgoing	LORS
LIMRID (Outgoing)	Outgoing	LORS
LLDUWR (Outgoing)	Outgoing	LORS
LIMRES LORS (Carrier)	Outgoing	LORS
LIMRES LORS (Broker)	Outgoing	LORS
RESETT	Incoming	Delinked Signing
LIMCLM Incoming	Incoming	Claims
LIMRIN	Incoming	LORS
LIMRID (Incoming)	Incoming	LORS
LLDUWR (Incoming)	Incoming	LORS

Full list of 22 bespoke EDIs committed

Mapping	MSG type
C_SCM_EDl_to_XML_Native_NoAttr	SCM001 (SCM)
C_SIGNIN_EDl_to_CSV_DL5001	BSM/IPCBSM
C_SIGNIN_EDl_to_RIF	BSM/IPCBSM
C_UCM_EDl_to_XML_Native_Noatt	MCM
C_USM_EDl_to_XML_Native_NoAttr	USM
C_UWRCON_EDl_to_ETS	MCM
C_UWRCON_EDl_to_INT	MCM
C_UWRSGN_EDl_to_ETS_V9	UWRSGN (USM)
C_UWRSGN_EDl_to_INT_V9	UWRSGN (USM)
C_LORS_XML_to_EDl_Native_NoAttr	LORS
C_Expand	<i>Review complete on 19 February</i>

Mapping	MSG type
C_Brit_Limclm_Edi_to_Sics	CLMLRC
C_BSM_EDl_to_XML_Native_NoAttr	SIGNIN - IPCBSM
C_LIMCLM_Edi_to_Rif	CLMLRC/CLMILC/CLMLRB/CLMILB/CLMLLB
C_LIMCLM_EDl_to_SICS	CLMLRC
C_LIMORI_EDl_to_ETS	LIMORI (LORS)
C_LIMORI_EDl_to_INT	LIMORI (LORS)
C_LORS_EDl_to_RIF	LIMORI (LORS)
C_LORS_EDl_to_XML_Native_NoAttr	LORS
C_SCM_EDl_to_ETS	SCM001 (SCM)
C_SCM_EDl_to_ETS_PAD13	SCM001 (SCM)
C_SCM_EDl_to_INT	SCM001 (SCM)

Full list of 18 ACORD4ALL EDIs committed

Mapping	MSG type
DSIGN_to_ACORD_2008_TechAccount_Mapping_v22.xlsx	DSIGN
DSIGN_to_ACORD_2010_2b_TechAccount_Mapping_v29	DSIGN
WSETT_to_Settlement_V7_2008.xls	WSETT
WSETT_to_ACORD_2010-2b Settlement_V11.xls	WSETT
ILUCSB_to_Settlement_2008_V2.xls	ILUCSB
ILUCSB_to_ACORD_2010-2b Settlement_V6.xlsx	ILUCSB
ILUCSM_to_ACORD_2008_TechAccount_Mapping_v15.xls	ILUCSM
ILUCSM_to_ACORD_2010_2b_TechAccount_Mapping_v29	ILUCSM
SCM_to_Acord_2008_v012.xls	SCM

Mapping	MSG type
SCM_to_Acord_2010-2b_ClaimMovement 26.xls	SCM
USM_to_Acord_2008_v011.xls	USM
USM_to_Acord_TechAccount_2010_2b_026.xls	USM
Lloyds_FA_from_TA_2010-2b Settlement_v6.xlsx	USM
LIRMA_Limclm_to_Acord_2010-2b ClaimMovement 23.xlsx	LIMCLM
Limclm_to_Acord_2008-1_018.xls	LIMCLM
C_LIMCLM EDI_to_XML_V2005_2	LIMCLM
ILU_Limclm_to_Acord_2008_v5.xls	LIMCLM
ILU_Limclm_to_Acord_2010_2b_ClaimMovement v15.xls	LIMCLM

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**We look forward to welcoming you to our
Working Session on 26 February 2024**